

# 規格書

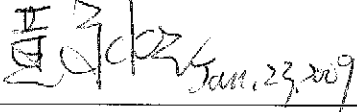
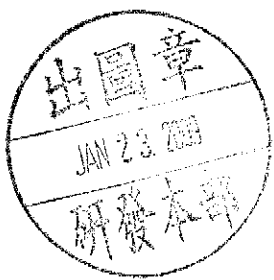
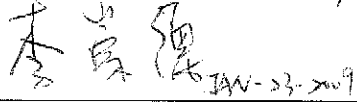
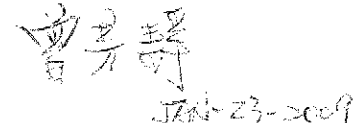
## SPECIFICATION

品名 **SWITCHING POWER SUPPLY**  
 STYLE NAME :

型號 **GSM-7450P**  
 MODEL NO. :

料號  
 PART NO. :

版次 **A3**  
 REVISION :

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## Revision

Rev.	Page	Item	Date	Description
A2	5	3.3.1	JUL-02-2008	Update specification
A3	5	3.3.1	JAN-23-2009	Update +5V output rating

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1.0 Scope

This specification defines the performance characteristics of a grounded, Ac input, 450 watts, 7 output level power supply. This specification also defines world wide safety requirements and manufactures process test requirements.

2.0 Input requirements

2.1 Voltage (sinusoidal) : 100~240 VAC full range(With  $\pm 10\%$  tolerance).

2.2 Frequency

The input frequency range will be 47hz~63hz.

2.3 Steady-state current

8A/4A at any low/high range input voltage.

2.4 Inrush current

40/60 Amps @ 115/230 VAC

2.5 Power factor correction

The power supply shall incorporate universal power input with active power factor correction, which shall reduce line harmonics in accordance with the EN 61000-3-2 standards.

PFC can reach the target of 95% @230VAC, Full load.

3.0 Output requirements

3.1 DC load requirements

Normal Output voltage	Load current(A)		Regulation tolerance	
	Min.	Max.	Max.	Min.
+5V	3	24	+5%	-5%
+12V	2	24	+5%	-5%
-5V	0	0.5	+5%	-5%
-12V	0	0.8	+5%	-5%
+3.3V	1	21	+5%	-5%
+24V	0	5	+5%	-5%
+5VSB	0.1	2	+5%	-5%

The output current of +5V & +3.3V not exceed 30A

The output power of +5V, +3.3V & +12V not exceed 310W

Maximum peak current for 24V is 7.5A for 20m sec.

When doing the cross regulation test (one output channel at high load and the other output channels at low load), it is requested to set the higher output channel at 80% max. of its spec., and the lower output channels at 20% max. of theirs.

### 3.2 Regulation

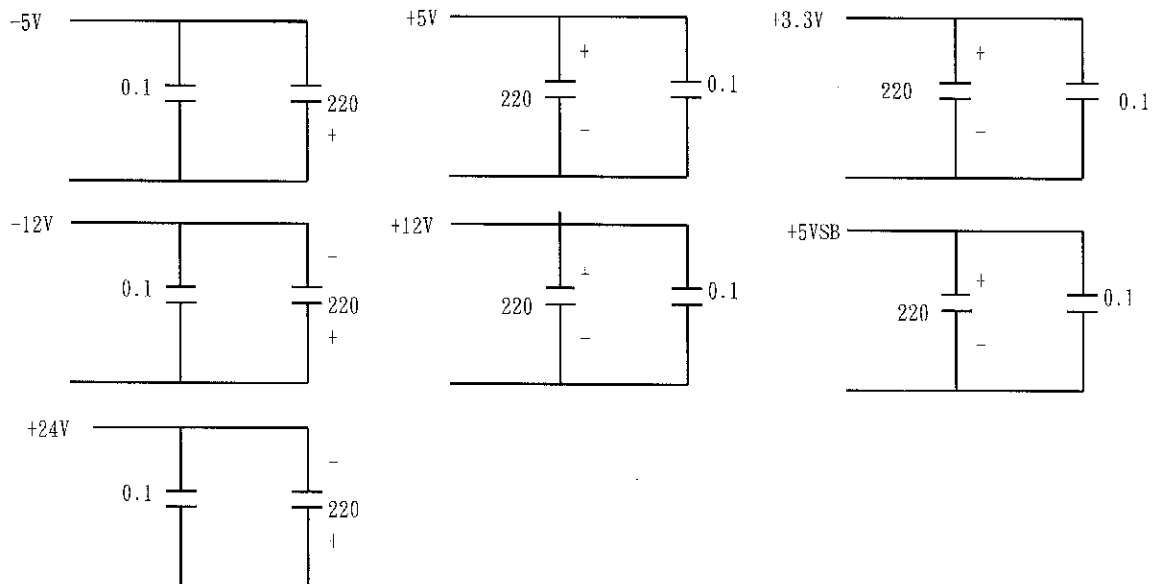
Output DC voltage	Line regulation
+5V	±1%
+12V	±1%
-5V	±1%
-12V	±1%
+3.3V	±1%
+24V	±1%
+5VSB	±1%

### 3.3 Ripple and noise

#### 3.3.1 Specification

+5V	50 mV (P-P)
+12V	120 mV (P-P)
-5V	120 mV (P-P)
-12V	120 mV (P-P)
+3.3V	50 mV (P-P)
+24V	200 mV (P-P)
+5VSB	50 mV (P-P)

#### 3.3.2 Ripple voltage test circuit



0.1uF is ceramic the other is tantalum.  
Noise bandwidth is from DC to 20MHz

### 3.4 Overshoot

Any overshoot at turn on or turn off shall be less 15% of the nominal voltage value, all output shall be within the regulation limit of section 3.2 before issuing the power good signal of section 6.0.

### 3.5 Efficiency

Power supply efficiency typical 70% at 115V, full load.

## 4.0 Protection

### 4.1 Input (primary)

The input power line must have an over power protection device in accordance with safety requirement of section 8.0

### 4.2 Output (secondary)

#### 4.2.1 Over power protection

The power supply shall provide over power protection on the power supply latches all DC output into a shutdown state. Over power of this type shall cause no damage to power supply, after over load is removed and a power on/off cycle is initiated, the power supply will restart.

Trip point total power min. 110%, max. 150%.

#### 4.2.2 Over voltage protection

If an over voltage fault occurs, the power supply will latch all DC output into a shutdown state.

	Min	Typical	Max
+3.3V	3.9V	4.1V	4.3V
+5V	5.7V	6.1V	6.5V
+12V	13.6V	14.3V	15.0V

#### 4.2.3 Short circuit

A short circuit placed on +5V, +3.3V, +12V, +24V, -5V, -12V output to DC return shall cause no damage and power supply latch.

#### 4.2.4 Restart after protection

After the protection mode (OPP/OVP) or short circuit is removed and a power on/off cycle is initiated, the power supply will restart. If the PSU is using AC power switch to control on/off, it has to be switched off at least for 5 seconds to restart the unit.

## 5.0 Power supply sequencing

### 5.1 Power on (see fig.1)

### 5.2 Hold up time

When power shutdown DC output 5V must be maintain 16msec in regulation limit at. normal input voltage

### 5.3 Power off sequence (see fig. 1)

## 6.0 Signal requirements

### 6.1 Power good signal (see fig. 1)

The power supply shall provide a "power good" signal to reset system logic, indicate proper operation of the power supply.

At power on, the power good signal shall have a turn on delay of at least 100ms but not greater than 500ms after the output voltages have reached their respective minimum sense levels.

7.0 Environment

7.1 Temperature

Operating temperature	0 to 50 degrees centigrade
Non-Operating temperature	-25 to 85 degrees centigrade
Safety regulation temperature	Applied at room temperature (25°C)

7.2 Humidity

Operating humidity	20% to 85%
Non-operating humidity	10% to 90%

7.3 Insulation resistance

Primary to secondary	: 20 meg. ohm min. 500 VDC
Primary to Frame Gnd	: 20 meg. ohm min. 500 VDC

7.4 Dielectric withstanding voltage

For approval purpose :

Primary to secondary	: 3KVAC for 1 min.
Primary to Frame Gnd	: 2200 VAC for 1 min.

For production purpose: 100% test

Primary to Frame Gnd	: 2200VAC for 1 sec
Cut off current	15mA

7.5 Leakage current

3.5 mA. max. at nominal voltage 250 VAC

8.0 Safety

8.1 Underwriters laboratory (UL).

The power supply designed to meet UL 60950.

8.2 Canadian standards association (CUL)

The power supply designed to meet CSA C22.2 No.60950.

8.3 TUV

The power supply shall be designed to meet TUV EN-60950.

8.4 CCC Standards

The power supply shall be designed to meet GB4943-1995,GB9254-1998, GB17625.1-1998.

9.0 Reliability

9.1 Burn in

All products shipped to customer must be burn in. The burn in shall be performed at high line voltage.

10.0 Mechanical requirements

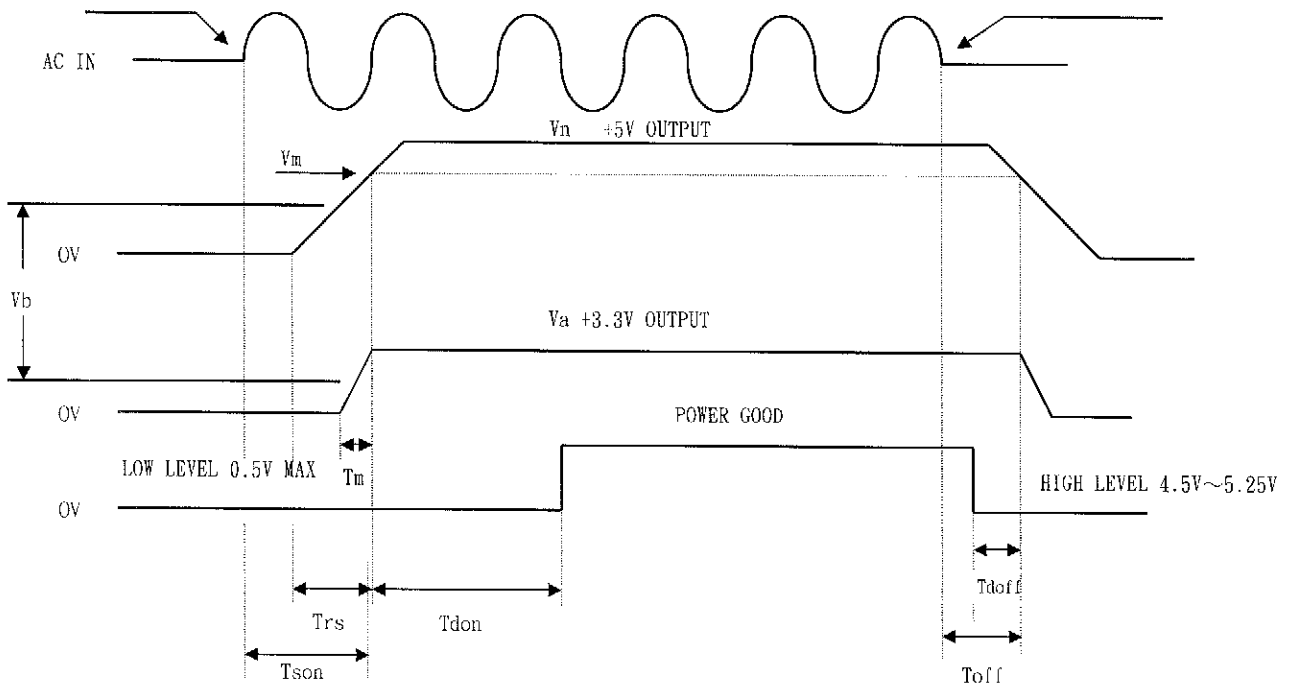
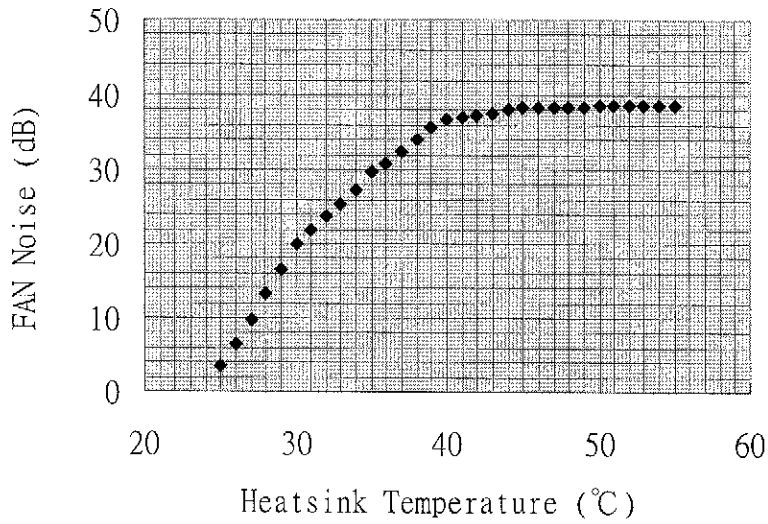
10.1 Physical dimension : 150 mm(D) \* 140 mm(W) \* 86 mm(H)



11.0 Fan speed control & noise

Fan speed is in varying with different temperature of heatsinks.

The relationship between fan noise and changes in temperature per shown in the following diagram.



- Vn Nominal voltages +5V
- Vm Minimum voltages +4.5V
- Va Nominal voltages +3.3V
- Vb +2.0V max
- Tson Switch on time (500 ms. max.)
- Trs +5V rise time (50ms. max.)
- Tdon Delay turn-on (100ms. < Tdon < 500ms.)
- Tdoff Delay turn-off (1 ms. min.)
- Toff Hold up time (16ms Min)

《Figure 1》